

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	LOW LEAKAGE MONOTONIC CMOS LOGIC							
Application Number :								
Date :								
First Named Applicant:	Kerry Bernstein							
Confirmation Number:								
Attorney Docket Number:	BUR920040024US1							
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<table border="1"><thead><tr><th>Submitted By:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Michael J. LeStrange Registered Number: 53,207</td><td>/Michael J. LESTRANGE, Reg. #53,207/</td><td>Attorney</td></tr></tbody></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Michael J. LeStrange Registered Number: 53,207	/Michael J. LESTRANGE, Reg. #53,207/	Attorney
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Documents being submitted:	Files
us-fee-sheet	BUR920040024US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-assignment	BUR920040024US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920040024US1assign1.tif BUR920040024US1assign2.tif
us-request	BUR920040024US1-usrequ.xml us-request.dtd us-request.xsl
application-body	BUR920040024US1SPEC-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd BUR920040024US1drawing1.tif BUR920040024US1drawing2.tif BUR920040024US1drawing3.tif BUR920040024US1drawing4.tif BUR920040024US1drawing5.tif BUR920040024US1drawing6.tif BUR920040024US1drawing7.tif BUR920040024US1drawing8.tif BUR920040024US1drawing9.tif BUR920040024US1drawing10.tif

us-declaration	BUR920040024US1declaration1.tif
us-declaration	BUR920040024US1declaration2.tif
Comments	
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